

Notice of References Cited

Application/Control No.

09/560,364

Applicant(s)/Patent Under
Reexamination
LO ET AL.

Examiner

Jason M Perilla

Art Unit

2634

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,026,051	02-2000	Keeth et al.	365/233
	B	US-5,852,378	12-1998	Keeth, Brent	327/171
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	National Semiconductor DS26C31 CMOS Quad TRI-STATE Differential Line Driver (Datasheet). 06-1998
	V	National Semiconductor DS26C32A Quad Differential Line Receiver (Datasheet). 06-1998
	W	Texas Instruments CDC111 1-Line to 9-Line Differential LVPECL Clock Driver (Datasheet). 09-1993
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.